

Trace D is the diode's current waveform. The diode provides a current path for the energy stored in the inductor to be transferred between the load and the output capacitor. When the diode is reverse biased, the output capacitor provides the load current. The LT1172's error amplifier compares the feedback pin voltage, from the 13k Ω –1.5k Ω divider, to its internal 1.24V reference and controls duty cycle. The output voltage can be varied by changing the R1–R2 divider ratio (see Equation 1). An RC network at the V_C pin provides loop compensation.

A charge pump is used to invert the +12V output to a –12V output. When the LT1172's power switch turns off, the voltage on C2's positive side rises until D1 is forward biased. The inductor charges C2 when the voltage on C2's negative side rises enough to forward bias D2. Trace F shows C2's current waveform, trace E is D2's voltage waveform and trace G is its current. The voltage across C2 will be equal to a diode drop above +V_{OUT} minus a Schottky diode drop. When the LT1172's power transistor turns on, the positive side of C2 is pulled to ground. During this period diode D3 is forward biased (trace H is its current waveform), and C4 is charged by C2. An optional LC filter is added to each output to attenuated output voltage ripple. Efficiency for this circuit generally exceeds 70%.

Diode junction losses (D2 and D3) preclude ideal results, but performance is quite good. This circuit will convert +V_{OUT} to –V_{OUT} with losses as shown in Figure 3. Negative output load current should not exceed the positive output load by more than a factor of 5, otherwise the imbalance will cause the –12V transient response to suffer.

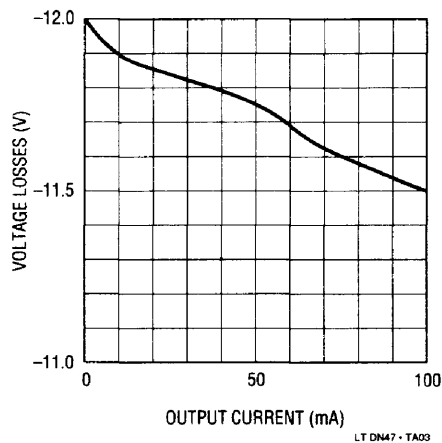


Figure 3. Losses for Charge Pump Converter

Figure 4 can be used for a LCD display contrast control. It is similar to the previous circuit except that all the load current is drawn from the negative output. This requires C3 to be small so negative output load fluctuations are quickly reflected to the positive output. Resistor R3 adjusts output voltage between –12V to –21V.

The LT1172 provides an elegant solution to power shutdown problems by integrating a shutdown feature; eliminating the need to place a power MOSFET in series with the input voltage. When the voltage of the V_C pin is pulled below 150mV, the IC shuts down pulling only 150 μ A. This is implemented by turning on Q1, reducing the circuit's quiescent current from 6mA to 150 μ A.

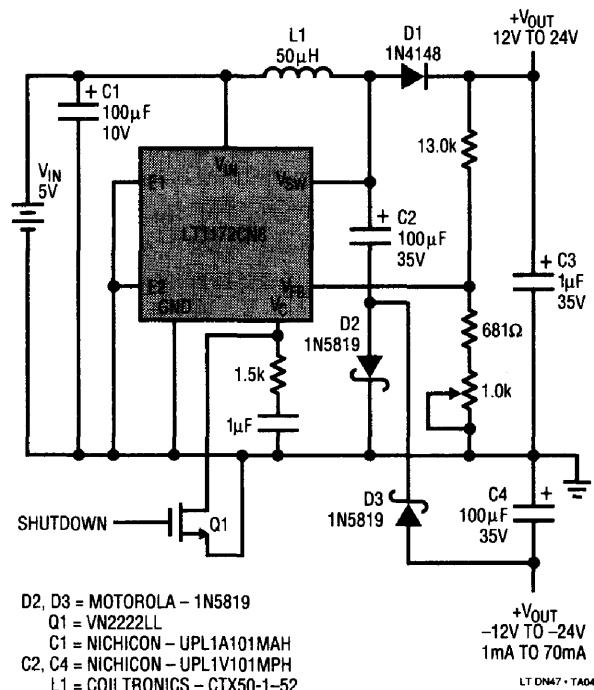


Figure 4. LCD Display Contrast Control Power Supply

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